

10/20/50

	Class	Subclass
ISSUE CLASSIFICATION		

PATENT NUMBER

U.S. **UTILITY** Patent Application

O.I.P.E.

PATENT DATE

SCANNED

Q.A.

APPLICATION NO. 09/851634	CONT/PRIOR D	CLASS 257	SUBCLASS C	ART UNIT -2811	EXAMINER W. H. ...
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APPLICANTS

Alan Reinberg

TITLE

Microelectronic device fabricating method, integrated circuit, and intermediate construction

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PTO-2040
12/99

ISSUING CLASSIFICATION												
ORIGINAL					CROSS REFERENCE(S)							
CLASS		SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
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			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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